# Action Taken 2024-25

| Course Code | Course Name | CO Average | CO Target | Gap | CO Observation | Action Taken |
|---|---|---|---|---|---|---|
| **CS302PC** | **Computer Organization and Architecture** | 1.56 | 1.75 | 0.19 | Students faced difficulties in key areas such as microprocessor architecture, instruction cycle execution, and memory organization concepts. Lower performance in these areas has contributed to the overall shortfall in CO attainment. | • Conducted Guest Lecture<br>• Implemented a flipped classroom strategy<br>• Conducted periodic slip tests to track progress. |
| **CS305PC** | **Discrete Mathematics** | 1.54 | 1.73 | 0.19 | Analysis of assessment results indicates that **students require additional practice** in applying abstract concepts such as *mathematical logic*, *set theory*, and *combinatorics* to solve problems.<br>Topics in **UNIT I (Mathematical Logic)** and **UNIT IV (Elementary Combinatorics)** were identified as areas where **enhanced reinforcement and application-based learning** would be beneficial. | • Conducted a **guest lecture** on "Discrete Structures in Computing" to strengthen conceptual clarity.<br>• Conducted periodic slip tests to track progress.<br>• Provided **STEP Material** |

## Gap Identification and Actions Taken for CO Attainment (A.Y 2023-24)

| Course Code | Course Name | CO Average | CO Target | Gap | CO Observation | Actions Taken |
|---|---|---|---|---|---|---|
| **CS103ES** | Programming for Problem Solving | 1.37 | 1.55 | 0.18 | Students showed difficulty in understanding pointers, dynamic memory, and manipulation of strings and structures. | - Conducted tutorial classes focusing on pointers. - Introduced PBL projects involving structures and arrays. - Guest lecture on file handling and pre-processor. - Regular lab coding sessions. |
| **ME204ES** | Engineering Graphics | 1.46 | 1.54 | 0.08 | Challenges in drawing 3D solids, especially frustums and truncated shapes. Common mistakes in true length and projections. | - Provided focused worksheets and guided tutorials. - Used 3D visual models and animations. - Conducted peer demonstration sessions. - Introduced sketching practice and time-bound tasks. |
| **CS403ES** | Analog & Digital Electronics | 1.54 | 1.72 | 0.18 | Students faced difficulty with FSM design, understanding sequential circuit behavior, and interpreting timing diagrams. | - Provided STEP Material - Taught using simple FSMs before progressing to complex ones. - Conducted peer review sessions for FSM design improvement. |
| **CS601PC** | Compiler Design | 1.56 | 1.72 | 0.16 | Weak understanding of syntax analysis and intermediate code generation. Superficial knowledge across compiler phases. | - Provided STEP Material - Provided video tutorials on compiler functionality. - Guest lecture by industry expert on compiler design. |